

Ch. Devi Lal State Institute of Engg. & Tech., Panniwala Mota (Sirsa)

Electrical Engineering Department

No. CDLSIET/EE/2020/ 32

Dated: 22.09.2020

Notice

It is informed to all the students of 7<sup>th</sup> semester (Electrical Engg.) that the practical examination of Semester (EE) (Main) under CDLU Sirsa will be conducted Online from 24.09.2020 to 28.09.2020 as per the schedule given below:-

Semester	Sr. No.	Date	Subject	Paper Code	Session/ Time
6 <sup>th</sup> semester (EE)	1.	24.09.2020	Advanced Programming Lab	EE-304-P	Morning (09:00am onwards)
	2.	25.09.2020	Power Electronics-II Lab	EE-306-P	Morning (09:00am onwards)
	3.	28.09.2020	Electric Drives and Traction Lab	EE-307-P	Morning (09:00am onwards)

1. All students must join the Google classroom of the respective subjects & Google meet on time for Online question paper and Online Viva-Voce.
2. During the answering of the question, the student must be on the device's camera on G- meet.
3. Question paper will be uploaded on the Google classroom well before the start of the Online Practical examination and student must upload the answer sheet well before the completion time of Written Practical Examination.
4. If there is any discrepancies, may bring in the notice of the undersigned for rectification.

  
Head (Electrical Engg.)

CC:

1. PA to Director-Principal-for kind information of Director-Principal
2. All concerned faculty
3. All Notice Boards (Institute/Hostels)
4. Programmer with a request to upload on the website