About Ch. Devi Lal State Institute of Engineering & Technology

Ch. Devi Lal State Institute of Engineering and Technology (CDLSIET) was established in 2003 by the Government of Haryana in the name of Chaudhary Devi Lal, former Chief Minister of Haryana, and Deputy Prime Minister of India. The institute was founded with the objective of promoting advanced studies and research in the field of engineering with a focus on emerging technologies and innovation. CDLSIET offers a four-year B.Tech. programme in various branches of engineering and technology. The academic environment is enriched by a team of experienced and dedicated faculty members specializing in diverse areas. To ensure holistic development of students, the institute regularly organizes a range of activities including extra cointellectual, and curricular, community engagement programmes.

About AICTE- VAANI Scheme

The All India Council for Technical Education (AICTE) has introduced the AICTE-Vibrant Advocacy for Advancement and Nurturing of Indian Languages (VAANI) scheme to promote the use of Indian languages in education. The initiative aims at enriching the teaching and learning experience by encouraging both educators and students to adopt Indian languages in technical education. The scheme was officially launched in March, 2024 at AICTE Headquarters in New Delhi by AICTE Chairman Prof. T.G. Sitharam, alongwith Padma Shri awardee and Chairman for Promotion of Indian Languages, Chamu Krishna Shastry.

About Electrical Engineering department

The Department of Electrical Engineering aims at equipping students with a strong foundation in key areas of discipline, including control systems, electrical machines, power system analysis, and power electronics. The department is furnished with well-equipped laboratories featuring latest equipment and technologies.

Programme Objectives

The primary objective of the workshop is to orient participants and provide them with comprehensive knowledge on various aspects of Smart Cities and E-Mobility, with a special focus on Hindi language. The workshop aims to train participants in latest technologies involved in the development of Smart Cities, which contribute to making urban spaces more livable, inclusive, and sustainable. Additionally, it will cover advancements in E-Mobility, including use of diverse energy sources and enhancements in flexibility and resilience of the transport sector. This initiative seeks to equip faculty members and researchers with the insights needed to engage in meaningful discussions on these topics and to guide students towards innovation-driven learning.

Envisioned Programme Outcomes

- To provide an introduction to concepts of Smart Cities and E-Mobility.
- To explore the potential and scope of renewable energy resources within Smart City framework.
- To highlight effective integration of power electronic devices and modules in Smart Cities and E-Mobility solutions.
- To promote the application of Artificial Intelligence (AI) tools to enhance city infrastructure and optimize resource management.

Who Can Attend

This workshop is open to faculty members from universities, colleges, and polytechnics offering AICTE-approved degree or diploma programmes. Research scholars, M.Tech. students, research fellows, and industry professionals are also encouraged to participate. **Location**

Ch. Devi Lal State Institute of Engineering & Technology, 22 KM Stone, NH-9, Sirsa-Dabwali Road, Village Panniwala Mota, Sirsa, Haryana – 125077.



Sponsored by: AICTE- Vibrant Advocacy for Advancement and Nurturing of Indian Languages (VAANI)



Organized at: Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)-125077

Accommodation

Separate accommodation will be provided to the outstation willing participants in respective Hostels of the Institute.

Venue of Workshop

Seminar Hall (Room no. 118), Ground Floor, Academic Block, Ch. Devi Lal State Institute of Engineering & Technology, 22KM Stone, NH-9, Sirsa-Dabwali Road, Village Panniwala Mota, Sirsa, Haryana-125077.

Registration Form

Participants are requested to register at the link/QR given below:

https://forms.gle/bnR2FpBkLoVvM5UZ8



Participants should be nominated by the Head of Institutions.

To join WhatsApp group, kindly click on the following link:

https://chat.whatsapp.com/F0L6a1za4dN 9Dl6gofc312

For further enquiry, contact

Sh. Puneet Chawla, Associate Professor & Head (EE) department (94167-88602) <u>directorcdlsiet@gmail.com</u>,

puneet1817@gmail.com,

- Registration will be on First come, First served basis.
- Registration will be **free of cost**.
- There will be no fee charged for any type of accommodation.
- Certificates will be awarded to all participants of the workshop.

PATRON

Dr. Meena Kumari Director-Principal, CDLSIET, Panniwala Mota (Sirsa)

CONVENER

Dr. Shyam Sunder Registrar, CDLSIET, Panniwala Mota (Sirsa)

ORGANIZING COMMITTEE COORDINATOR

Sh. Puneet Chawla Associate Professor & Head, EE Deptt. CO-CORDINATOR

Dr. Suman Rani Assistant Professor, ECE department MEMBERS

Dr. Sanjay Dahiya, Asso. Prof. CSE Sh. Gaurav Singh Sisodia, Asso. Prof. Maths Sh. Pawan Kumar, Asso. Prof. ME Sh. Manik Goval, Asst. Prof. Civil Engg. Ms. Privanka Handa, Asst. Prof. ECE Dr. Rubrinder Singh, Asst. Prof. FT Dr. Vikas Gupta, Asst. Prof. ME Sh. Ashok Kumar Garg, Asst. Prof. ME Ms. Sita Devi, AP, EE Sh. Raman Kamboj, AP, EE Sh. Sanjay, AP, EE Dr. Arushi Bansal, AP, CSE Dr. Manju Devi, AP, CSE Dr. Vidhu Kiran, AP, CSE Dr. Anil Kumar, AP, ME Ms. Manju Bala, AP, Civil Engg. Mr. Kamaldeep Singh, AP, Civil Engg. Dr. Sonu Jakhar, AP, Chemistry **Computer Operating Team**

Ms. Ruby Sathiala, AP, EE Sh. Surinder Kumar, Clerk, CSE Sh. Vikaram, Clerk, TPO Cell

Important Dates

10th JulySolution11th JulySolution

Registration Last Date 08th July, 2025

CONFIRMATION BY EMAIL IMMEDIATELY AFTER REGISTRATION



Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa) -125077