

**Ch. Devi Lal State Institute of Engineering & Technology,
Panniwala Mota (Sirsa)**

**Electrical Engineering Department
Time Table: ODD Semester (2025-2026)**

Name of Faculty: Dr. Jagdish Kumar						Total Load: 09 Hours			
LECTURE	1	2	3	4		5	6	7	8
DAY	09:00-09:55	09:55-10:50	10:50-11:45	11:45-12:40	12:40-01:20	01:20-02:15	02:15-03:10	03:10-04:05	04:05-05:00
MONDAY	ECN 3 rd Sem EE 213			ECN (T) G1 3 rd Sem. EE, 213	L	Minor Project (G2) 7 th Sem. 222			
TURSDAY	ECN 3 rd Sem EE 213			ECN (T) G2 3 rd Sem. EE, 213	U				
WEDNESDAY	ECN 3 rd Sem EE 213				N				
THURSDAY					C				
FRIDAY					H				

(Signature)
Head (Electrical Engg.)
CDL State Institute of
Engg. & Tech
Panniwala Mota (Sirsa)

**Ch. Devi Lal State Institute of Engineering & Technology,
Panniwala Mota (Sirsa)**

**Electrical Engineering Department
Time Table: ODD Semester (2025-2026)**

Time Table: CDD Semester (2023-2024)

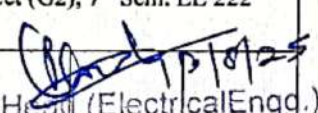
Name of Faculty: Sh. Puneet Chawla						Total Load: 16 Hours			
LECTURE	1	2	3	4		5	6	7	8
DAY	09:00-09:55	09:55-10:50	10:50-11:45	11:45-12:40	12:40-01:20	01:20-02:15	02:15-03:10	03:10-04:05	04:05-05:00
MONDAY	EEM 5 th Sem. EE, 212	PSOC 7 th Sem. EE, 212			L			Elec. Workshop (G2) 3 rd Sem. EE 202	
TURSDAY			PSOC 7 th Sem. EE, 212		U	PSOC (T) (G2) 7 th Sem. EE 221	PSOC (T) (G1) 7 th Sem. EE 221	Elec. Workshop (G1) 3 rd Sem. EE 202	
WEDNESDAY			Library 5 th Sem. EE		N	Minor Project (G1) 7 th Sem. EE, 222			
THURSDAY	EEM 5 th Sem. EE, 212	PSOC 7 th Sem. EE, 212			C				
FRIDAY		EEM 5 th Sem. EE, 212			H				

(Signature)
Head (Electrical Engg.)
CDL State Institute of
Engg. & Tech
Panniwala Mota (Sirsa)



**Ch. Devi Lal State Institute of Engineering & Technology,
Panniwala Mota (Sirsa)
Electrical Engineering Department
Time Table: ODD Semester (2025-2026)**

Name of Faculty: Ms. Sita Devi						Total Load: 20 Hours			
LECTURE	1	2	3	4		5	6	7	8
DAY	09:00-09:55	09:55-10:50	10:50-11:45	11:45-12:40	12:40-01:20	01:20-02:15	02:15-03:10	03:10-04:05	04:05-05:00
MONDAY	UEE 7 th Sem. EE, 222			EM-I (T) G2 3 rd Sem. EE (202)	L	EM-I Lab (G1) 3 rd Sem. EE 201			
TURSDAY		UEE 7 th Sem. EE, 222		EM-I (T) G1 3 rd Sem. EE (202)	U	EM-I Lab (G2) 3 rd Sem. EE 201			
WEDNESDAY			UEE 7 th Sem. EE, 222		N		EM-I 3 rd Sem EE, 213		
THURSDAY		EM-I 3 rd Sem EE, 213			C	Minor Project (G2), 7 th Sem. EE 222			
FRIDAY	EM-I 3 rd Sem EE, 213				H				


 Head (Electrical Engg.)
 CDL State Institute of
 Engg. & Tech
 Panniwala Mota (Sirsa)

**Ch. Devi Lal State Institute of Engineering & Technology,
Panniwala Mota (Sirsa)
Electrical Engineering Department
Time Table: ODD Semester (2025-2026)**

Name of Faculty: Ms. Ruby Sathiala						Total Load: 22 Hours			
LECTURE	1	2	3	4		5	6	7	8
DAY	09:00-09:55	09:55-10:50	10:50-11:45	11:45-12:40	12:40-01:20	01:20-02:15	02:15-03:10	03:10-04:05	04:05-05:00
MONDAY			BEE 1 st Sem. EE+ECE+ME+FT, 313		L	APE&D (G1) 5 th Sem EE 114		APE&D (G2) 5 th Sem EE 114	
TURSDAY			BEE 1 st Sem. EE+ECE+ME+FT, 313		U	BEE (T) 1 st Sem (G1). EE, 114	BEE Lab 1 st Sem. EE (G1) 114		
WEDNESDAY	APE&D 5 th Sem. EE, 221		BEE 1 st Sem. EE+ECE+ME+FT, 313		N	BEE (T) 1 st Sem. ME, 114	BEE Lab 1 st Sem. ME 114		
THURSDAY			APE&D 5 th Sem. EE, 221		C	BEE (T) 1 st Sem (G2). EE, 114	BEE Lab 1 st Sem. EE (G2) 114		
FRIDAY			APE&D 5 th Sem. EE, 221		H	BEE (T) 1 st Sem ECE+FT, 114	BEE Lab 1 st Sem. ECE+FT 114		


 Head (Electrical Engg.)
 CDL State Institute of
 Engg. & Tech
 Panniwala Mota (Sirsa)

**Ch. Devi Lal State Institute of Engineering & Technology,
Panniwala Mota (Sirsa)
Electrical Engineering Department
Time Table: ODD Semester (2025-2026)**

Name of Faculty: Ms. Shivani Jindal						Total Load: 24 Hours			
LECTURE	1	2	3	4		5	6	7	8
DAY	09:00-09:55	09:55-10:50	10:50-11:45	11:45-12:40	12:40-01:20	01:20-02:15	02:15-03:10	03:10-04:05	04:05-05:00
MONDAY			EDC 3 rd Sem. EE, 212	μ P & μ C 5 th Sem. EE, 221	L	EDC Lab (G2) 3 rd Sem. EE 202			
TURSDAY	FOM 5 th Sem EE+ECE+ME 221			μ P & μ C 5 th Sem. EE, 221	U	EDC Lab (G1) 3 rd Sem. EE 202			
WEDNESDAY		μ P & μ C 5 th Sem. EE, 221			N	IT-II (G2) 7 th Sem. EE, 212			
THURSDAY	EDC 3 rd Sem. EE, 212				C	IT-II (G1) 7 th Sem. EE, 212			
FRIDAY	FOM 5 th Sem EE+ECE+ME 221		EDC 3 rd Sem. EE, 212		H	μ P & μ C Lab (G2) 5 th Sem. EE, 202		μ P & μ C Lab (G1) 5 th Sem. EE, 202	

(Signature)
Head (Electrical Engg.)
CDL State Institute of
Engg. & Tech
Panniwala Mota (Sirsa)

**Ch. Devi Lal State Institute of Engineering & Technology,
Panniwala Mota (Sirsa)
Electrical Engineering Department
Time Table: ODD Semester (2025-2026)**

Name of Faculty: Sh. Raman Kamboj						Total Load: 20 Hours			
LECTURE	1	2	3	4		5	6	7	8
DAY	09:00-09:55	09:55-10:50	10:50-11:45	11:45-12:40	12:40-01:20	01:20-02:15	02:15-03:10	03:10-04:05	04:05-05:00
MONDAY		CS -I 5 th Sem. EE 221	Personality Development (PDP) 5 th Sem EE		L	Minor Project (G1) 7 th Sem EE, 222		RE Lab (G1) 7 th Sem. EE, 222	
TURSDAY		CS -I 5 th Sem. EE, 221			U	Minor Project (G1) 7 th Sem EE, 222		RE Lab (G1) 7 th Sem. EE, 222	
WEDNESDAY		GEP 3 rd Sem. EE, 213			N			CS-T (T) G1 5 th Sem. EE, 221	
THURSDAY		CS -I 5 th Sem. EE			C		GEP 3 rd Sem. EE, 213	CS-T (T) G2 5 th Sem. EE, 221	
FRIDAY		GEP 3 rd Sem. EE, 213			H	CS-I Lab (G1) 5 th Sem. EE 201		CS-I Lab (G2) 5 th Sem. EE 201	

(Signature)
Head (Electrical Engg.)
CDL State Institute of
Engg. & Tech
Panniwala Mota (Sirsa)

**Ch. Devi Lal State Institute of Engineering & Technology,
Panniwala Mota (Sirsa)**

Electrical Engineering Department

Session: 2025-2026

Time Table: ODD Semester (2025-2026)

Name of Faculty: Sh. Sanjay						Total Load: 20 Hours			
LECTURE	1	2	3	4		5	6	7	8
DAY	09:00-09:55	09:55-10:50	10:50-11:45	11:45-12:40	12:40-01:20	01:20-02:15	02:15-03:10	03:10-04:05	04:05-05:00
MONDAY	EMA 7 th Sem CE		EMA 7 th Sem EE, 222	EMA 7 th Sem CSE	L	IT-I (G2) 5 th Sem EE, 212			
TUESDAY	EMA 7 th Sem EE, 222			EMA 7 th Sem CSE	U	IT-I (G1) 5 th Sem EE, 212			
WEDNESDAY	PDS 7 th Sem EE	EMA 7 th Sem CE		PDS 7 th Sem EE	N				
THURSDAY	EMA 7 th Sem CE		EMA 7 th Sem EE, 222	PDS 7 th Sem EE	C				
FRIDAY		EMA 7 th Sem CSE			H				


 13/8/25
 Head (Electrical Engg.)
 CDL State Institute of
 Engg. & Tech
 Panniwala Mota (Sirsa)