



ONE WEEK FACULTY DEVELOPMENT PROGRAMME (CONTACT MODE)

on
“**MATLAB AND SIMULINK PROGRAMMING**”

from
27th October, 2025 – 31st October, 2025

PATRON

Prof. (Dr.) Bhola Ram Gurjar
Director
NITTTR Chandigarh

CHAIRPERSON

Dr. Jagdish Kumar
Director-Principal
CDLSIET Panniwala Mota, Sirsa

NITTTR Chandigarh Coordinator

Dr. Lini Mathew
Professor, Electrical Engg.
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Institute Coordinator

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Institute Co-Coordinator

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Electrical Engg., CDLSIET Panniwala Mota, Sirsa

Organized & Funded by:

ELECTRICAL ENGINEERING DEPARTMENT

**National Institute of Technical Teacher's Training and Research (NITTTR),
Chandigarh-160019**

(Ministry of Education, Government of India)

Venue:

Ch. Devi Lal State Institute of Engineering & Technology, Panniwala Mota (Sirsa)-125077

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ABOUT NITTTR, CHANDIGARH

National Institute of Technical Teachers Training & Research, (NITTTR) at Chandigarh is one of the four NITTTRs, started in collaboration with Royal Netherlands Government in 1967 and is registered under the Societies Registration Act, 1860 and managed by a Board of Governors.

The Institute started with long term teachers' training programmes for polytechnic teachers in 1967 and was also entrusted with responsibility of promoting curriculum development work for states in the region. Since June 2001, this institute has been conducting short term courses in various subjects for faculty of Engineering Colleges/Polytechnics and M.E. degree programmes in various streams are also being offered for teachers of technical institutions and their administrators, professionals from industry and general candidates, and are duly approved by AICTE. The Institute has been recognized as a Research Centre by Panjab University, Chandigarh as well as by Panjab Technical University, Jalandhar (Punjab) for pursuing research work leading to the degree of Doctor of Philosophy i.e. Ph.D. Looking into the consistent performance and contribution in technical education system, the institute has now been recently granted the status of Deemed to be University under District Category with effect from 22 February 2024.

ABOUT THE ORGANIZING DEPARTMENT

Electrical Engineering Department is one of the major departments, created since the establishment of NITTTR Chandigarh. With the institute being raised to the level of National Institute, the department had been conducting a large number of courses in the various emerging areas for the faculty members of Engineering Colleges/Polytechnics all over the country. As a result of the various staff development programmes, the trained teachers have gradually elevated to senior positions and have by now taken up higher responsibilities. The department has also been very active in conducting the need based short term training programmes every year as per the institute Operational plan. Since last 24 years, the department has been very successfully offering Master of Engineering Course in Electrical Engineering with specialization in Instrumentation and Control. In addition, the department is also conducting ME Electrical Engineering (Instrumentation & Control) Modular Programme since 2005. The department was granted the status of National Resource Centre (NRC), under the ARPIT Scheme in which two MOOCs courses were launched on SWAYAM platform. Another course on PLC and Microcontroller in Hindi Language was developed and launched on SWAYAM Platform, which ran successfully thrice and has again been accepted for rerun. The Institute being a Research Centre for Panjab University, Chandigarh, 16 research scholars acquired their Ph.D degree and presently 5 scholars are pursuing their research work in various areas of Electrical Engineering. The institute after granted the status of Deemed to be University under District Category, 6 scholars are pursuing their research work.

ABOUT CDLSIET, PANNIWALA MOTA

Ch. Devi Lal State Institute of Engineering and Technology (CDLSIET) was established in 2003 by the Government of Haryana in the name of Chaudhary Devi Lal, former Chief Minister of Haryana, and Deputy Prime Minister of India. The institute was founded with the objective of promoting advanced studies and research in the field of engineering with a focus on emerging technologies and innovation. CDLSIET offers a four-year B.Tech. programme in various branches of engineering and technology. The academic environment is enriched by a team of experienced and dedicated faculty members specializing in diverse areas. To ensure holistic development of students, the institute regularly organizes a range of activities including extra co-curricular, intellectual, and community engagement programmes.

ABOUT ELECTRICAL ENGINEERING DEPARTMENT, CDLSIET, PANNIWALA MOTA

Electrical Engineering Department is one of the major departments, created since the establishment of the institute. The department is conducting B.Tech. Course with an intake of 60 seats along with 10% LEET seats for deserving candidates across the country. With the Institute being raised, the department had been established various laboratories with a large number of equipments/ kits and machines in the various course curriculum and emerging areas of Electrical Engineering. As a result, various students had been placed and working in esteemed organizations throughout the country and across the world and have by now taken up higher responsibilities. For the last 18 years, the department has been very successfully offering the courses and facilitated the students to achieve their goal of life with success and hard work. The department faculty is actively involved in R&D, live/ Industry-Institution Interaction based projects, completed 09 Sponsored projects, Industrial Training as well as Industrial visit to explore the Electrical Engineering field as per today's trends and need of the society.

PROGRAMME OBJECTIVES

- To apprise the participants about programming using MATLAB and simulation tool SIMULINK.
- To demonstrate the use of different Toolboxes like Deep Learning, Fuzzy Logic, Control System and different block sets of SIMULINK.
- To train participants by providing assignment sheets for practice and evaluating at the end of the course.

The program will have 70% practice sessions

COURSE CONTENTS

- MATLAB Programming
- Various Structures and Programming capabilities
- Toolboxes like Deep Learning, Fuzzy Logic, Control System
- Use of SIMULINK as a simulation tool.
- Simulation of Power Electronic Circuits using SIMULINK.
- Additional practice sessions of MATLAB/ SIMULINK programming

WHO CAN ATTEND?

Faculty members/technical personnel of all branches of Engineering and even of Applied Science from Colleges/ Universities / Polytechnics offering degree/diploma programmes approved by AICTE. *All registered participants must bring their laptops with them during this FDP.*

REGISTRATION

Registration will have to be done on the Online Training Management System of NITTTR Chandigarh. The link for registration is:

<https://fdp.nitttrchd.ac.in/fdp2025/index.php>

Registration Last Date & Fee: 26th Oct., 2025.

Rs.590/- (Rs.500/- +18% GST) as Registration and certification fees will be charged from the participants.

Minimum 80% attendance is mandatory for certification

For more details, contact

NITTTR COORDINATOR

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INSTITUTE COORDINATOR

Sh. Puneet Chawla
Associate Prof. EE Deptt.
94167-88602



One Week

Faculty Development Programme
(Contact Mode)

On

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